EEDG/CE 6303: Testing and Testable Design (Spring’2023) Department of Electrical & Computer Engineering

The University of Texas at Dallas

**Instructor: Mehrdad Nourani (nourani@utdallas.edu)**

**Cover Page for All Submissions**

**(Assignment, Project, Codes/Simulations/CAD, Examinations, etc.)**

**Last Name (as shown in the official UT Dallas Student ID Card): Annadata**

**First Name: Yagna Srinivasa Harsha**

**Submission Materials for (e.g. Homework #, Project #): Homework6**

**Statement of Academic Honesty**

I certify that:

1. the attached report (for assignment, project, codes/simulations/CAD, examinations, etc.) is my own work, based on my personal study and/or research,
2. I have acknowledged all material and sources used in its preparation, whether they be books, articles, reports, lecture notes, and any other kind of document, electronic or personal communication,
3. this report has not previously been submitted for assessment in EEDG/CE 6303 or any other course at UT Dallas or elsewhere,
4. I have not copied in part or whole or otherwise plagiarized the work of other students and/or persons, and
5. I have read and understood the Department and University policies on scholastic dishonesty as outlined in: [http://www.utdallas.edu/deanofstudents/dishonesty/.](http://www.utdallas.edu/deanofstudents/dishonesty/)

Name: Yagna Srinivasa H Annadata Date: 5/5/2023

Signature: